

Call for Participation

**DoD High Performance Computing Modernization Program (HPCMP),
Distributed Center (DC) Working Group meeting,
Tuesday, August 15, 2000**

Followed by

**An Advanced Parallel Software Development and Tuning Workshop,
Wednesday, August 16 - Friday August 18, 2000**

The Maui High Performance Computing Center (MHPCC) will host a DoD HPCMP, Distributed Center (DC) Working Group meeting on Tuesday, August 15, 2000 at its facility in Kihei, HI. This session is designed to address HPCMP-specific issues of relevance to Distributed Centers. Additional information on the agenda will be forthcoming. If you have topics that you would like to have addressed at this meeting, please provide them to Steve Karwoski via e-mail to karwoski@nhpcc.edu.

Immediately following the DC working group meeting, MHPCC will host a three day Advanced Parallel Software Development and Tuning Workshop that will start on August 16. The three-day workshop will focus on scalable, parallel technologies and feature papers and presentations by HPCMP users in addition to a focused session by IBM's Advanced Computational Technical Center (ACTC). The format of the workshop will have the HPCMP user presentations during the first one and one-half days followed by the ACTC presentation.

Call for Papers

If you would like to present a topic at the workshop, we would request that you provide an abstract by July 1, 2000. Abstracts should be concisely written and no longer than 150 words. Authors should address the computational science aspects of their topic to include novel approaches to parallelization, optimization, debugging, visualization, or portability. Each oral presentation should be 30 to 45 minutes, including 5 minutes for questions.

Presentations will be selected on the basis of their relevance to the HPCMP community; focus on scalable, parallel technologies; originality, and clarity. Notification of acceptance will be provided by July 15, 2000.

Topics of interest include:

All abstracts are to be submitted to Steve Karwoski via e-mail to karwoski@mhpcc.edu. Abstracts may be provided in plain text or as an MS Word document attachment.

ACTC Presentation

The ACTC presentation will be conducted on August 17 - 18 starting in the afternoon of August 17th. This portion of the workshop will focus on specific optimization strategies for IBM parallel architectures.

The ACTC training session will be composed of a selection of the following topics:

- (1) Uni-processor optimization: cache and FPU tuning
- (2) Shared memory optimization: OpenMP and Pthreads
- (3) Distributed memory optimization: MPI and LAPI
- (4) Development Tools: Debugging and Profiling
- (5) Hybrid Model Programming: MPI + OpenMP / Pthreads
- (6) I/O optimization: async I/O, MPI-IO, and ACTC MIO library

Travel Arrangements

A block of 25 rooms have been reserved at the Maui Coast Hotel for workshop participants at a rate of \$94 + tax per room for a standard room and \$104 + tax per room for an Alcove Suite. These rates are for single or double occupancy. Check in date is booked for Monday, August 14 and check out on Sunday, August 20. Last date for calling in reservations is Sunday, July 16 - anything after that will be on space availability w/no guarantee on preferred group rate.

All reservations need to be made with Maui Coast's central reservation line- (800) 895-6284. The Room block is specifically named "HPCMP." A credit card number is required to guarantee the room.

For additional information, please contact Steve Karwoski at (808) 879-5077 extension 228 or via e-mail at karwoski@mhpcc.edu.